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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/929,591	08/14/2001	Louis L. Hsu	728-216 (YOR9-2001-0444 U	9143
75	590 10/05/2005		EXAMINER	
Paul J. Farrell, Esq.			NGUYEN, DANNY	
DILWORTH & BARRESE LLP 333 Earle Ovington Boulevard			ART UNIT	PAPER NUMBER
Uniondale, NY 11553			2836	

DATE MAILED: 10/05/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

			At
- <del></del>	Application No.	Applicant(s)	
	09/929,591	HSU ET AL.	
Office Action Summary	Examiner	Art Unit	<del></del>
	Danny Nguyen	2836	
The MAILING DATE of this communication ap	opears on the cover sheet wi	th the correspondence address	s
Period for Reply  A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION  - Extensions of time may be available under the provisions of 37 CFR 1.  after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a report of the period for reply is specified above, the maximum statutory period  - Failure to reply within the set or extended period for reply will, by stature Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).  Status	136(a). In no event, however, may a r ply within the statutory minimum of thirt d will apply and will expire SIX (6) MON te, cause the application to become AB	eply be timely filed by (30) days will be considered timely. ITHS from the mailing date of this community BANDONED (35 U.S.C. § 133).	nication.
1) Responsive to communication(s) filed on 13	April 2005.		
· · · · · · · · · · · · · · · · · · ·	is action is non-final.		
3) Since this application is in condition for allows	ance except for formal matt	ers, prosecution as to the mer	rits is
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D	). 11, 453 O.G. 213.	
Disposition of Claims		•	
4) Claim(s) 2-6,8-11,13-16 and 18-22 is/are pen	nding in the application.		
4a) Of the above claim(s) is/are withdra	awn from consideration.		
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>2-5,8-11,13-15 and 18-22</u> is/are reje	ected.		
7)⊠ Claim(s) <u>6 and 16</u> is/are objected to.			
8) Claim(s) are subject to restriction and/	or election requirement.		
Application Papers			
9)☐ The specification is objected to by the Examin	ner.		
10) The drawing(s) filed on is/are: a) ac	cepted or b) objected to	by the Examiner.	
Applicant may not request that any objection to the	• • • • • • • • • • • • • • • • • • • •		
Replacement drawing sheet(s) including the corre	·		
11) The oath or declaration is objected to by the E	Examiner. Note the attached	d Office Action or form PTO-1	52.
Priority under 35 U.S.C. § 119			
<ul> <li>12) Acknowledgment is made of a claim for foreig</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documer</li> <li>2. Certified copies of the priority documer</li> <li>3. Copies of the certified copies of the priority application from the International Burea</li> <li>* See the attached detailed Office action for a list</li> </ul>	nts have been received. nts have been received in A ority documents have been au (PCT Rule 17.2(a)).	pplication No received in this National Stag	ie
Attachment(s)			
Notice of References Cited (PTO-892)		Summary (PTO-413)	
<ul> <li>Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08</li> </ul>		s)/Mail Date nformal Patent Application (PTO-152)	)
Paper No(s)/Mail Date	6) Other:		

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#### **DETAILED ACTION**

1. In a telephone interview with Mr. Musella on 9/19/2005, Mr. Musella indicated that claims 6 and 16 which are rejected under Hsu et al are improper since the subject matter of the reference and the claimed invention were subject to an obligation of assignment to the same person at the time the invention. The rejection of claims 6 and 16 is withdrawn. Therefore, the previous office action will be vacated. The time period for the new office action is reset to run from the mailing date of this office action.

# Response to Arguments

2 Applicant's arguments filed 4/13/2005 have been fully considered but they are not persuasive.

Regarding the amended claims 10 and 20, applicant argued that Keeth does not teach the enable register is configured for isolating each of the plurality of macros from the external voltage supply. Examiner respectfully disagrees with the applicant's argument. Keeth discloses a memory device (DRAM) comprises a plurality of arrays (10). If one of the pluralities of arrays is found defect, it is independently electrically isolated from the remaining circuitry (col. 3, lines 19-30); and the memory device further comprises a programmable elements (such as register), which allow for isolating the defective arrays (col. 3, lines 58-64, and col. 9, lines 8-18). Thus, applicant's arguments with respect to claims 10 and 20 do not distinguish over the Keeth reference. However, Examiner agrees with applicant that both Keeth and Itoh do not teach the enable register which is coupled to the voltage limiter, the oscillator, and the charge pump enables or disables at least the limiter, the oscillator, and the pump. However,

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applicant's amendment necessitated the new ground(s) of rejection presented in this Office action.

### Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3 Claims 2-5, 9-11, 13-15, 18-22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Keeth (USPN 5,946,257) in view of Kowalski (USPN 5,444,412).

Regarding claims 2, 8-11, 18-22, Keeth discloses an integrated circuit system (see fig. 1 and 8) having a plurality of macros (memory arrays 801(1) to 801(8)), the integrated circuit comprises an external voltage supply input (the external supply input voltage Vcc) configured to supply an external voltage to the integrated circuit; and a plurality of internal voltage supply generators (e.g. 804 (1) to 804 (8)), each connected to a respective macro and configured for receiving the external voltage for generating an internal voltage supply for operating its respective macro (e.g. col. 9, lines 55-60), and each of the plurality of internal voltage supply generators including at least one reference supply (such as 1004 shown fig. 10) for generating the voltage level, an enable register is configured for storing one of enable and disable signal for isolating each of the plurality of macros from the power supply (col. 3, lines 58-64, and col. 9, lines 8-18). Keeth does not disclose the generator, and feedback as claimed. Kowalski discloses a generator comprises a voltage limiter (e.g. voltage regulator in figure 2) for

controlling the voltage level, an oscillator (col. 4, lines 54-57), a charge pump (such as charge pump PMP in figure 2), an enable register (such the logic circuit CL) for enabling or disabling at least the limiter, the oscillator, and the pump according the stored signal (col. 4, lines 18-35), and a feedback (VHT) from the pump to the limiter. It would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified the generator device of Keeth to incorporate enable register which couples to the voltage limiter, an oscillator, and a charge pump and feedback signal as disclosed by Kowalski in order to provide a stable operating voltage level in memory device.

Regarding claim 3, Keeth discloses the external voltage is greater than the internal voltage (the internal supply voltage is approximately one half of the external supply voltage Vcc, see col. 9, lines 9, line 58).

Regarding claims 4, 13, 21, Keeth discloses a scan-chain formed by a chain of scannable register latches storing fuse information and switch enable/disable signal (see col. 4, lines 53-57).

Regarding claims 5, 14, 15, Keeth discloses each of the plurality of internal voltage generators comprises a reference voltage generator (1004) for generating and providing a reference voltage for driving at least one voltage generator (see fig. 10).

## Allowable Subject Matter

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Claims 6 and 16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

#### Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Danny Nguyen whose telephone number is (571)-272-2054. The examiner can normally be reached on Mon to Fri 8:00 AM to 4:30 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on (571)-272-2058. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DN

DN 9/22/2005

BRIAN SIRCUS
SUPERVISORY PATENT EXAMINER

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